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Sammak, Amir; Aminian, Mahdi; Nanver, Lis K.; Charbon, Edoardo

*Published in:*  
IEEE Transactions on Electron Devices

*DOI (link to publication from Publisher):*  
[10.1109/TED.2015.2457241](https://doi.org/10.1109/TED.2015.2457241)

*Publication date:*  
2016

*Document Version*  
Publisher's PDF, also known as Version of record

[Link to publication from Aalborg University](#)

*Citation for published version (APA):*  
Sammak, A., Aminian, M., Nanver, L. K., & Charbon, E. (2016). CMOS-compatible PureGaB Ge-on-Si APD pixel arrays. *IEEE Transactions on Electron Devices*, 63(1), 92-99. [7331281].  
<https://doi.org/10.1109/TED.2015.2457241>

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# CMOS-Compatible PureGaB Ge-on-Si APD Pixel Arrays

Amir Sammak, Mahdi Aminian, Lis K. Nanver, *Member, IEEE*, and Edoardo Charbon, *Senior Member, IEEE*

**Abstract**—Pure gallium and pure boron (PureGaB) Ge-on-Si photodiodes were fabricated in a CMOS compatible process and operated in linear and avalanche mode. Three different pixel geometries with very different area-to-perimeter ratios were investigated in linear arrays of 300 pixels with each a size of  $26 \times 26 \mu\text{m}^2$ . The processing of anode contacts at the anode perimeters leaving oxide covered PureGaB-only light-entrance windows, created perimeter defects that increased the vertical Ge volume but did not deteriorate the diode ideality. The dark current at 1 V reverse bias was below  $35 \mu\text{A}/\text{cm}^2$  at room temperature and below the measurement limit of  $2.5 \times 10^{-2} \mu\text{A}/\text{cm}^2$  at 77 K. Spread in dark current levels and optical gain, that reached the range of  $10^6$  at 77 K, was lowest for the devices with largest perimeter. All device types were reliably operational in a wide temperature range from 77 K to room temperature. The spectral sensitivity of the detectors extended from visible to the telecom band with responsivities of 0.15 and 0.135 A/W at 850 and 940 nm, respectively.

**Index Terms**—Avalanche photodiode (APD), Ge-on-Si, near-infrared photodiode, pure gallium and pure boron (PureGaB).

## I. INTRODUCTION

AVALANCHE photodiodes (APDs) and single-photon avalanche diodes (SPADs), or Geiger-mode APDs, are widely used in optical telecommunications, imaging, and medical diagnostics, where high sensitivity to light in the visible or near-infrared (NIR) ranges is needed [1]. Silicon photomultipliers (SiPMs) and imaging arrays of APDs and SPADs have seen a fast growth in the last decade, and are used today in a variety of applications, including time-of-flight imaging, positron emission tomography, fluorescence microscopy, and Raman spectroscopy, to name a few [2], [3]. Nevertheless, photomultiplier tubes, the predecessors

of SiPMs, are still the devices of choice in many applications due to their good timing resolution and ease of use.

Silicon (Si) photodetectors and CMOS SPADs cannot be used at infrared wavelengths because of the large bandgap that makes them only suitable for wavelengths shorter than  $1.1 \mu\text{m}$ . These sensors in fact exhibit high sensitivity in the visible range usually peaking at 450–500 nm.

For sensitivity at NIR wavelengths, germanium (Ge) and direct bandgap semiconductors, such as HgCdTe, and the III–V compounds InGaAs/InP are generally used due to their narrower bandgap that gives good absorption properties and performance in this range [4]. These compound materials have a tunable bandgap, allowing detection into the short-wave infrared to the very long wave infrared regions, but none of them have been integrated in CMOS. Ge APDs are cheaper to produce, and have been available for decades, but it has been problematic to fabricate them with the desired sensitivity in the NIR range and sufficiently low noise levels over a wide frequency band. Nevertheless, Ge is attractive because it has been possible to develop processes allowing integration in CMOS, and low-quality Ge photodiodes have been demonstrated in telecom circuits [5], [6]. For high-quality devices, the lattice mismatch between Si and Ge has meant a fundamental incompatibility with the standard CMOS processing, preventing the implementation of low-cost photon counters and arrays.

The pure gallium and pure boron (PureGaB) Ge-on-Si process, that is the topic of this paper, is an example of a CMOS compatible technology for fabricating high-quality Ge photodiode arrays. In [7]–[10], the fabrication of these photodiodes was described in detail. In short, single-crystalline islands of Ge are selectively deposited in oxide windows to a Si substrate. The Ge is n-doped *in situ* with arsenic (As), and to form a  $p^+$ -region on this n-Ge, the deposition cycle is continued with the growth of 1-nm-thin layer-stack of PureGaB. Both Ga and B are p-type dopants for Ge, and the layer stack of PureGaB has been shown to deliver an ultrashallow junction with low saturation current even though the junction depth is only a few nanometer.

In this paper, the optical characterization of the PureGaB Ge-on-Si photodiodes is investigated for the first time at temperatures from 77 to 300 K. In [7], we describe how a new metallization approach for the fabrication of the PureGaB Ge-on-Si photodiodes was implemented to achieve an oxide covered PureGaB-only light-entrance window on each photodiode in a  $300 \times 1$  array in order to obtain the maximum photon absorption. The study was limited to material

Manuscript received March 15, 2015; revised June 3, 2015; accepted July 11, 2015. Date of publication November 18, 2015; date of current version December 24, 2015. This work was supported in part by the Netherlands Agency, IOP Photonics Devices, through RASKIN Project, and in part by the Swiss Research Program Nano-Tera. The review of this paper was arranged by Editor S. Kawahito. (Amir Sammak and Mahdi Aminian equally contributed to this work.)

A. Sammak is with the Department of Microelectronics, Delft University of Technology, Delft 2628 CT, The Netherlands (e-mail: a.sammak@tudelft.nl).

M. Aminian is with the Department of Electrical Engineering, École Polytechnique Fédérale de Lausanne, Lausanne 1015, Switzerland (e-mail: mahdi.aminian@epfl.ch).

L. K. Nanver is with Aalborg University, Aalborg 9220, Denmark, and also with the University of Twente, Enschede NB 7522, The Netherlands (e-mail: l.k.nanver@utwente.nl).

E. Charbon is with the Delft University of Technology, Delft 2628 CT, The Netherlands (e-mail: e.charbon@tudelft.nl).

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Digital Object Identifier 10.1109/TED.2015.2457241

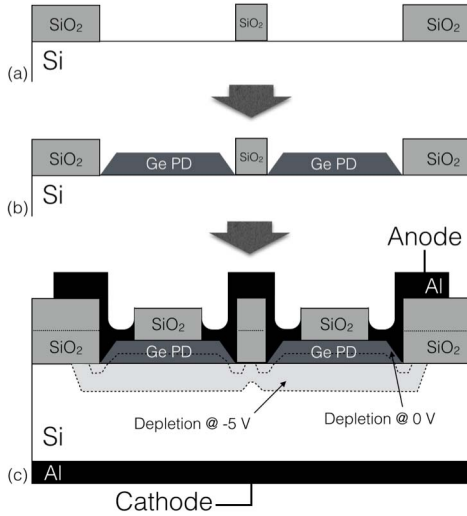


Fig. 1. Schematic of the process flow for fabricating PureGaB Ge-on-Si diodes with the metallization approach presented in [7]. (a) Windows are opened through  $\text{SiO}_2$  to Si. (b) PureGaB Ge-on-Si photodiodes are deposited in one deposition cycle in the windows. (c) Al-contacts to the anode and cathode regions are made along with  $\text{SiO}_2$ -covered PureGaB-only light-entrance windows.

analysis and electric characterization of the diodes, and revealed that aluminum (Al) contacting of the PureGaB at the diode perimeter could become a tradeoff between preserving the PureGaB barrier to the Al-contact metal and having low series resistance. Nevertheless, it was possible to maintain a good electrical performance with low dark currents and good uniformity across the arrays of  $300 \times 1$  devices even when the PureGaB was penetrated by the Al. While the Si-to-Ge interface contained a very low defect level after the PureGaB deposition [8], this contact processing increased defect levels at the diode perimeter. The low dark-current levels that were nevertheless achieved have been ascribed to the damage-free processing of the  $p^+$ -anode regions. Photodiodes of different sizes and geometries were realized with the good quality  $I$ - $V$  characteristics in terms of the ideality factors and dark currents that, to the best of our knowledge, are among the lowest reported in the literature for Ge and Ge-on-Si diodes [9]. The devices described in this paper were suitable for operation both in linear and avalanche mode. The linear arrays of 300 pixels were designed and fabricated as part of a project targeting low-cost medical diagnostics with Raman spectroscopy in the NIR [11]. Other potential applications include time-of-flight imaging and NIR optical tomography. The optical characterization confirms the electrical result that the arrays have high reproducibility, and the peak sensitivity is at  $\sim 850$  nm with a peak optical gain at 940 nm.

## II. DEVICE DESIGN AND FABRICATION

Fig. 1 shows a schematic of the fabrication process of the PureGaB Ge-on-Si arrays. Arrays of  $300 \times 1$  pixels were integrated on substrates of 2–5  $\Omega\text{-cm}$  n-type (100) Si wafers. First, a 30-nm thermal  $\text{SiO}_2$  is grown on the Si surface followed by a low-pressure CVD  $\text{SiO}_2$  layer deposition with a thickness of  $\sim 1$   $\mu\text{m}$ . The  $\text{SiO}_2$  layer was patterned and

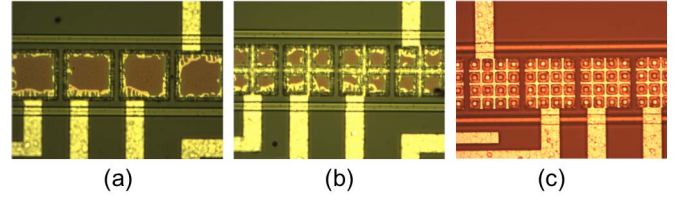


Fig. 2. Optical images of (a) single-diode pixel of  $26 \times 26 \mu\text{m}^2$ , (b) 4-diode quadpixel composed of  $12 \times 12 \mu\text{m}^2$  Ge-on-Si photodiodes, and (c) 16-diode hexapixel composed of  $5 \times 5 \mu\text{m}^2$  Ge-on-Si photodiodes.

TABLE I  
GEOMETRICAL PARAMETERS OF THE THREE DIFFERENT  
PHOTODIODE PIXELS

	No. of diodes / pixel	Diode size [ $\mu\text{m}$ ]	Total diode area in pixel [ $\mu\text{m}^2$ ]	Total diode perimeter in pixel [ $\mu\text{m}$ ]
Single pixel	1	$26 \times 26$	676	104
Quad pixel	4	$12 \times 12$	576	192
Hexa pixel	16	$5 \times 5$	400	320

etched on the Si surface by a mask that defined the Ge-on-Si photodiode areas. As illustrated by the microimages of Fig. 2, the arrays were designed with three different pixel geometries: 1) a single-diode pixel of size  $26 \times 26 \mu\text{m}^2$ ; 2) a 4-diode pixel (quad) composed of  $12 \times 12 \mu\text{m}^2$  Ge diodes; and (3) a 16-diode pixel (hexa) composed of  $5 \times 5 \mu\text{m}^2$  Ge diodes. All the pixels had the same total area of  $26 \times 26 \mu\text{m}^2$ . However, since the diodes in the multidiode devices were separated by a 1- $\mu\text{m}$ -wide oxide region, the actual Ge area decreases with the number of diodes while the Ge perimeter increases. The exact geometrical design parameters of the PureGaB photodiodes are listed in Table I. Moreover, around each of the  $300 \times 1$  pixel arrays, a set of two 5- $\mu\text{m}$ -wide lines were patterned to prevent Ge deposited on surrounding oxide regions from reaching the windows designed for the deposition of the As-doped Ge islands. Such undesirable loading effects could otherwise lead to very nonuniform deposition across the array [8]. To also counteract the nucleation of Ge on the  $\text{SiO}_2$ , the surrounding open Si area was maximized. These precautions also reduce the loading effects and the growth-rate nonuniformities of the *in situ* As n-doping [8]. The defect-density of the crystalline Ge that is deposited with this approach is shown to be in the order of  $10^7 \text{ cm}^{-2}$  [12].

The devices were then transferred to a plasma-enhanced CVD (PECVD) reactor for deposition of an 800-nm- $\text{SiO}_2$  layer, which was then patterned with 1- $\mu\text{m}$ -wide windows along the perimeter of the Ge-on-Si photodiodes. These windows were opened by plasma etching with soft landing on the PureGaB. Next, 800-nm Al/Si1% was sputtered and then removed over the photosensitive junctions by means of selective plasma etching of the Al layer to the oxide covering the PureGaB. As a last step, the metal was alloyed for 1 h at 400  $^\circ\text{C}$  in forming gas to achieve H-passivation of the Si/ $\text{SiO}_2$  interface.

From the images in Fig. 2 of the fabricated pixels, it can be seen that the Al removal on the oxide-covered light-entrance

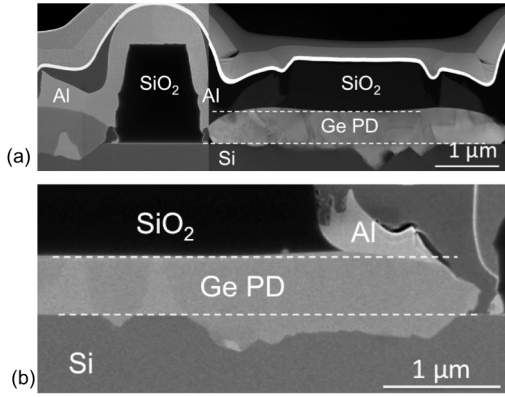


Fig. 3. (a) TEM image of a 16-diode hexapixel composed of  $5 \times 5 \mu\text{m}^2$  Ge-on-Si photodiodes. (b) TEM image of the edge of a single-diode pixel of  $26 \times 26 \mu\text{m}^2$ .

windows was incomplete in regions near the Al tracks to the measurement pads. This was due to reflections from the Al during lithographical exposure of the resist. The Al remnants were much thinner than the as-deposited Al, and the optical absorption at the long wavelengths used here was not of significance.

The most critical step in the processing was found to be the etching of the contact windows through the PECVD  $\text{SiO}_2$  with landing on the PureGaB layer [Fig. 1(c)]. The high-resolution transmission electron microscopy (TEM) images, as shown in Fig. 3(a) and (b), revealed that the oxide is likely to be etched along the Ge-island perimeter down close to the Si substrate. This can result in the tip of Ge itself-without protection from the PureGaB layer-being exposed to the Al metallization. This phenomenon was studied in detail in [7]. A migration of Al through Ge was observed that induced a displacement of Ge and Si, up to  $5 \mu\text{m}$  from the edge, resulting in Ge-filled V-grooves protruding into Si at the Ge-Si interface. These grooves increased the vertical thickness of the Ge layer. As can be seen by comparing Fig. 3(a) and (b), it is clear that the Ge volume per diode area is larger for the multidiode devices both due to the larger thickness at the edge of the as-grown Ge crystal from loading effects and due to the Al-induced Ge migration. The thickness of Ge at the center of the  $5 \times 5 \mu\text{m}^2$  diode was  $0.55 \mu\text{m}$  while it was  $<0.54 \mu\text{m}$  for the  $26 \times 26 \mu\text{m}^2$  device. Therefore, while the surface area of the hexapixels was only 60% of that of the single-diode pixels, the volume per diode area increased due to the three times longer perimeter. This increase could be as high as 30%.

### III. ELECTRICAL CHARACTERIZATION

A continuous-flow cryostat system was used for achieving low temperatures. The cryogenic probe station (PMC150, SUSS MicroTech AG) was used for manually probing chips that were cooled in the cryostat system. Vacuum conditions were maintained to be free from ice and frosting on the chip surface even for temperatures as low as 77 K achieved with liquid nitrogen cooling. The Ge-on-Si photodiode samples were mounted with thermal grease onto a cold mount at the end of the refrigerator. This gives an excellent thermal contact between the sample and the fridge.

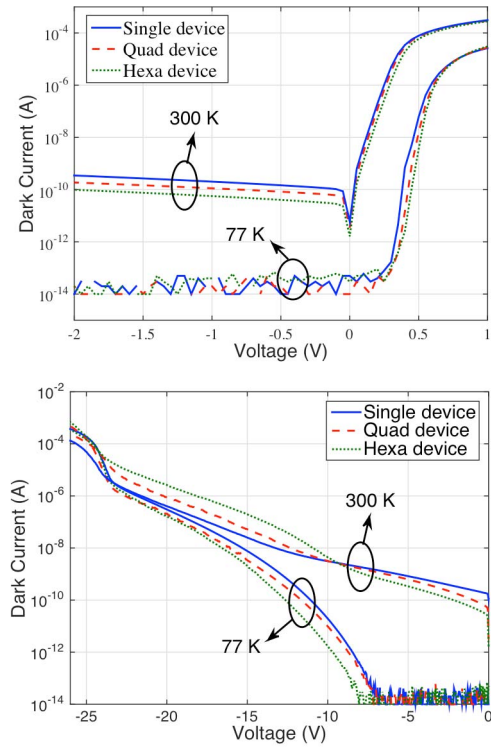


Fig. 4.  $I$ - $V$  characteristics at 300 and 77 K for the three pixel designs.

The diode  $I$ - $V$  characteristics were measured using a HP 4145B semiconductor parameter analyzer. Typical  $I$ - $V$  characteristics are shown in Fig. 4 for both room temperature and 77 K measurements of all three device types. The dark-current ranges from 1 nA at room temperature to well under the measurement limit of the setup (10 fA) at 77 K at a reverse bias of up to 6 V.

Ideally, the current would follow the Ge area as concluded in [7] on the basis of statistical measurements over the wafer. For devices with the lowest saturation current, compared with the single-diode device, the quad device had  $\sim 30\%$  lower current and the hexa device had  $\sim 60\%$  lower current. Over the arrays, a spread in currents that included values up to about a decade higher than the most ideal devices was identified, and could be related to the Al-migration through the Ge from the Al-exposed edge of the island.

The devices on the wafer that were chosen for the optical measurements discussed in this paper have a slightly higher n-doping of the Ge than those studied in [7], giving a slightly lower breakdown voltage of about 24 V instead of 30 V. The spread in the  $I$ - $V$  characteristics is also more prominent over the whole wafer and less ideally low saturation currents were identified. This is particularly the case for the hexa devices that have a very large perimeter length. This can be seen from Fig. 5, where the  $I$ - $V$  characteristics at 77 K are shown for 70 devices from four different arrays of different pixel types. Nevertheless, while the hexapixels displayed relatively high current levels, the spread in current was lower than that of other pixel types. This can be understood in terms of the defects related to the parasitic processes at the Ge-island perimeter. The perimeter of the hexa device was 3 times longer

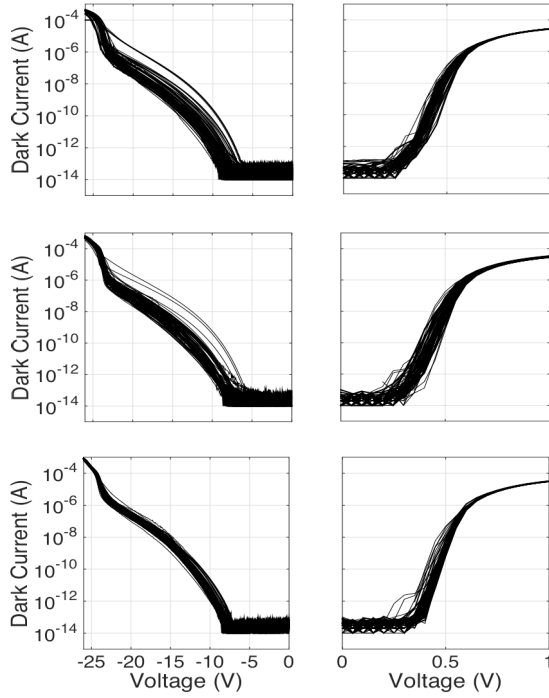


Fig. 5. Dark current (left) and forward current (right) spread across 70 devices measured at 77 K. Top: single pixels. Middle: quad pixels. Bottom: hexapixels.

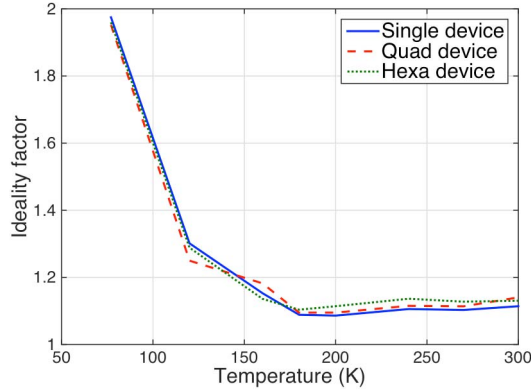


Fig. 6. Ideality factor for three different device types as a function of temperature.

than that of the single-diode pixel, i.e., it became less likely that a perfect device would be found.

The presence of defects that could become a source of nonideal currents was evident when considering the ideality factor  $n$  given by

$$I = I_0(e^{qV/nkT} - 1) \quad (1)$$

where  $I_0$  is the saturation current and  $q/kT$  is the thermal voltage at temperature  $T$ . With an increasing defect density,  $n$  will increase from  $n = 1$  for ideal junctions to  $n = 2$  for junctions containing a high level of generation–recombination centers. In Fig. 6,  $n$  is plotted as a function of temperature. The values are extracted from the slope of the forward  $I$ – $V$  characteristics, and were  $\sim 1.12$  at room temperature for all three types of pixels despite the very long total perimeter of

the hexa devices. Thus, it can be concluded that the electrically active defect density is low, even at the perimeter where Al-migration takes place. At 77 K, the ideality factors increased to  $\sim 2$ , indicating that generation–recombination currents are dominating. Nevertheless, the low  $n$  values at room temperature and very low dark currents substantiate that the Ge deposition, as well as the anode formation and contacting, are of good quality. The depletion width in reverse will quickly become larger than the  $0.55 \mu\text{m}$  thickness of the Ge-islands and transverse the nonperfect interface with the Si. However, the distribution of dark current levels between the pixel types does not display any correlation to the decreasing Ge diode area when going from the single diode to hexa design, so that the Ge-to-Si interface defects do not appear to play any decisive role.

In all the cases, the device dark current was gradually increasing with reverse bias, leading to a gradual breakdown. Breakdown is governed by a large number of factors; in particular, the way the depletion region expands through Ge into the Si is of importance. The Ge thickness that varies from the edge of the island to the center will be decisive for the depletion width since the Ge doping is  $\sim 10$  times higher than that of the Si substrate. Moreover, the Al-migration at the Ge-island edge will not only increase the Ge thickness but also result in coalescence of the Al into a small number of grains that can be found up to  $\sim 5 \mu\text{m}$  from the oxide edge. These can function as the Schottky diodes from which the n-region is also depleted [7]. When Al travels through Ge or Si, it can also be incorporated as a p-dopant. In Si, this was found to occur in the Al-induced solid-phase-epitaxy (SPE) process described in [13], by which the Si was doped to the Al solid solubility of  $10^{18}$ – $10^{19} \text{ cm}^{-3}$  for anneal temperatures from  $400^\circ\text{C}$  to  $500^\circ\text{C}$ . On n-Si, this Al-doped SPE Si was used to form the anode of good quality  $p^+n$  diodes [13]. The solid solubility of Al in Ge is much higher, about  $4 \times 10^{20} \text{ cm}^{-3}$  at  $400^\circ\text{C}$  [14]. Therefore, not only Al grains but also Al-doped  $p^+$ -regions can form junctions to the n-Ge crystal. These phenomena could also account for the increase and spread in saturation current and reverse leakage current.

#### IV. OPTICAL CHARACTERIZATION

The Ge photodiodes were mounted in the vacuum chamber and cooled to 77 K. For accurate results, it is important that the photodiode arrays be uniformly exposed to light. Responsivity was measured at several wavelengths, using laser diodes as light sources (M660F1, M850F1, and M940F1 from THORLABS) with wavelengths of 660, 850, and 940 nm, respectively. The corresponding output power of laser sources was 14.5, 7.7, and 6.5 mW, respectively. However, the incident optical power on the devices was attenuated, resulting in 9.8, 0.75, and  $0.78 \mu\text{W}$  for the respective laser sources. The diameter of the incident beam spot was  $0.3 \text{ cm}$ .

The photocurrent was measured using a computer-controlled HP Semiconductor Parameter Analyzer model 4145B. A computer with ICCAP data acquisition was used to record the photocurrent through a general-purpose interface bus to the HP4145B. Neutral density filters were used to prevent light saturation and pileup.



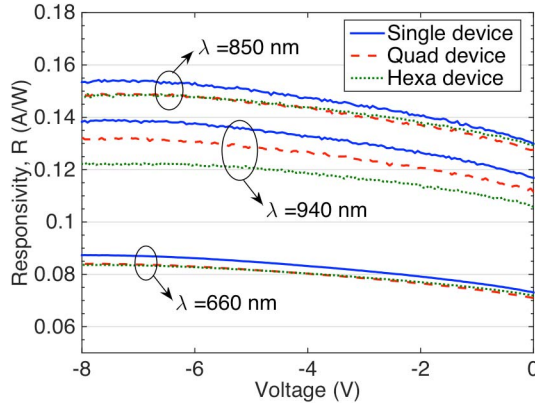


Fig. 7. Responsivity  $R$  measured at 77 K for three different device types.

TABLE II

SUMMARY OF OVERALL BEST PERFORMANCE COMPARED WITH SEVERAL Ge PHOTODIODE DESIGNS IN THE LITERATURE

	PureGaB Ge diode	Ref [17]	Ref [18]	Ref [19]	Ref [20]
Ge Thickness ( $\mu\text{m}$ )	0.6	1.1	0.4	Bulk Ge	2
Dark current density ( $\mu\text{A}/\text{cm}^2$ ) @-1 V, 300 K	35	19	1500	62	400 @ -2 V, 263 K
$R$ (A/W) @77 K	0.150 @850nm 0.135 @940nm	0.55 @1300 nm	0.18 @850 nm	0.55 @1550 nm	-
QE (%) @77 K	21.9% @850nm 17.8% @940 nm	52% @1300 nm	27% @850 nm	45% @1550 nm	42% @1300 nm, 193 K

The responsivity  $R$  as a function of reverse voltage  $V_x$  was calculated as

$$R(V_x) = (I_{\text{ph}, V_x} - I_{\text{dark}, V_x})/P \quad (2)$$

where the photocurrent  $I_{\text{ph}, V_x}$  and the dark current  $I_{\text{dark}, V_x}$  are measured at  $V_x$  and  $P$  is the incident power at the surface of the pixel. In Fig. 7, the extracted  $R$  at 77 K is plotted for all three types of devices and available wavelengths. The  $P$  is calculated assuming a pixel area of  $26 \times 26 \mu\text{m}^2$  in all cases. The responsivity values are comparable with what is commonly found for Ge detectors, a few values for which are listed in Table II and compared with our single diode  $26 \times 26 \mu\text{m}^2$  device. The responsivity generally increases with wavelength, and the peak responsivity in Ge is found at a wavelength slightly lower than the cutoff wavelength at  $\sim 1.6 \mu\text{m}$ . Nevertheless, here,  $R$  at 940 nm is slightly lower than at 850 nm. This behavior can be explained in terms of the absorption length of light in Ge that is 100, 320, and 500 nm for the incident wavelengths of 660, 850, and 940 nm, respectively. The absorption increases with increasing depletion of the Ge, as can be seen here for a voltage range from 0 to  $\sim -6$  V. At about  $-6$  V, the Ge is completely depleted. The absorption in Si is negligible compared with that

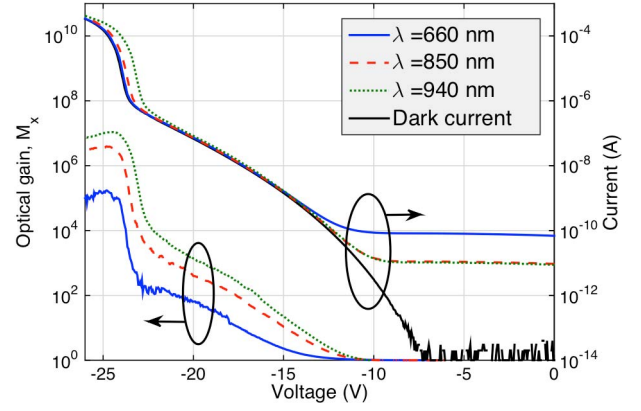


Fig. 8. Optical gain as a function of reverse voltage for three different wavelengths at 77 K for a single-diode pixel, and the corresponding  $I$ - $V$  characteristics for both the illuminated and nonilluminated cases.

in Ge, hence with the 500-nm absorption length of 940-nm light, a relatively large amount of light will pass through the Ge without being detected. At 0 V, 660- and 850-nm light resulted in photocurrents with essentially no device geometry dependence, and the quad and hexa devices maintain the same photocurrent through to  $-8$  V. At this voltage, however, the single-diode device had a nearly 5% higher photocurrent. For a wavelength of 940 nm, the increase was  $\sim 10\%$ . Considering that the Ge-diode area is almost halved when going from the single diode to the hexa device, the small variation of the photocurrent with area suggests that a significant amount of stray peripheral light is picked up at the diode perimeter, separated by only  $1\text{-}\mu\text{m}$ -wide oxide hills in the multidiode devices. Another factor that may play a role is the higher Ge thickness at the perimeter of the diodes but if this was the decisive factor a larger increase in  $R$  would be expected with increasing  $V_x$  that almost doubles the Ge volume.

The gradual breakdown of the diodes makes them suitable for operation as APDs. The high sensitivity of an APD relies on using avalanche multiplication to generate an internal signal gain. In this mode, the absorbed photon generates electron-hole pairs that are multiplied thus increasing the initial photocurrent flow. The total current was measured for the three types of Ge-on-Si photodiodes with the reverse bias voltage going from 0 to  $-26$  V while the devices were illuminated by the laser.

The optical gain  $M_x$  at reverse voltage  $V_x$  was calculated here as

$$M_x = (I_{\text{ph}, V_x} - I_{\text{dark}, V_x}) / (I_{\text{ph}, 0} - I_{\text{dark}, 0}) \quad (3)$$

where  $I_{\text{ph}, V_x}$ , and  $I_{\text{dark}, V_x}$  were measured at a high reverse voltage  $V_x$  while  $I_{\text{ph}, 0}$  and  $I_{\text{dark}, 0}$  were measured at a small reverse bias of  $-7$  V, where the depletion over the junction covers the whole Ge crystal but the leakage current is not yet influenced by impact ionization. The photocurrent for different illuminations and the dark current, both measured at 77 K for the  $26 \times 26 \mu\text{m}^2$  single-diode device, are shown in Fig. 8, along with  $M_x$  that peaks at  $-24$  V with more than  $10^6$  at wavelengths of 940 and 850 nm. This underlines the fact that the dark current, even when impact ionization events

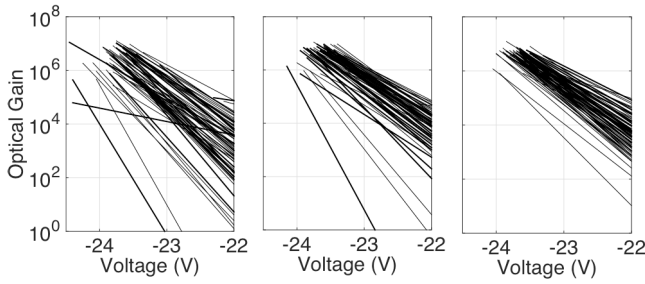


Fig. 9. Fitted optical gain spread across 70 devices measured at 940 nm and 77 K. Left: single pixels. Center: quad pixels. Right: hexapixels.

dominate, is very low. In line with a higher responsivity at the NIR wavelengths, the gain also displayed higher values than at visible wavelengths, with 940 nm yielding the highest gain. This suggests that the photoevents occurring near the metallurgic junction, where the electric field is highest, create more electron-hole pairs than deeper photoevents, presumably because not only the holes, but also the electrons, will achieve a high velocity when created in the high field region. The responsivity was measured at low voltage where it was slightly lower at 940 nm than at 850 nm because of the limited thickness of the Ge islands. In bulk Ge, however, the responsivity generally will increase linearly with wavelength at least up to 1000 nm so if multiplication is highest at the surface, the 940 nm wavelength will give the highest gain. This effect is an advantage for the targeted applications in the NIR region and may improve performance at wavelengths up to  $1.6 \mu\text{m}$  despite the limited Ge thickness.

The responsivity at 660 nm is mainly not reported for Ge photodiodes. In other types of devices with tens-of-nanometer-thick implanted or epitaxially grown light-entrance windows, it will also be very low because of the low absorption length in Ge at visible wavelengths [15]. For our PureGaB devices where the  $p^+$ -anode is only a few nanometer thick, the optical gain and responsivity remains high even at 660 nm, about 0.085 A/W, which is about the same as the responsivity of the PureB Si photodiodes at a wavelength of 400 nm where the penetration depth also reduces to 100 nm [16].

The leakage current of the devices is related to the defect density that may also influence the impact ionization process. To evaluate whether there was a connection, the gain-to- $V_{bd}$  relationship was determined for 70 devices of each geometry from Fig. 5. Lin-log fits of the optical gain as a function of voltages close to breakdown are shown in Fig. 9. They reveal a spread that appears to be smallest for the hexa devices; nevertheless, the gain is comparable for all geometries. The difference in optical gain from device type to device type is, therefore, likely to be related to the spread in dark current rather than to the geometrical effects.

This trend is corroborated by Fig. 10, which plots the maximum optical gain as a function of  $V_{bd}$ . From this plot, it is clear that, although the breakdown voltage is similar for all three pixel geometries, the spreading is lowest for the hexapixel array. This is also true for the spread in maximum optical gain. The standard deviations are represented in Fig. 10 by the

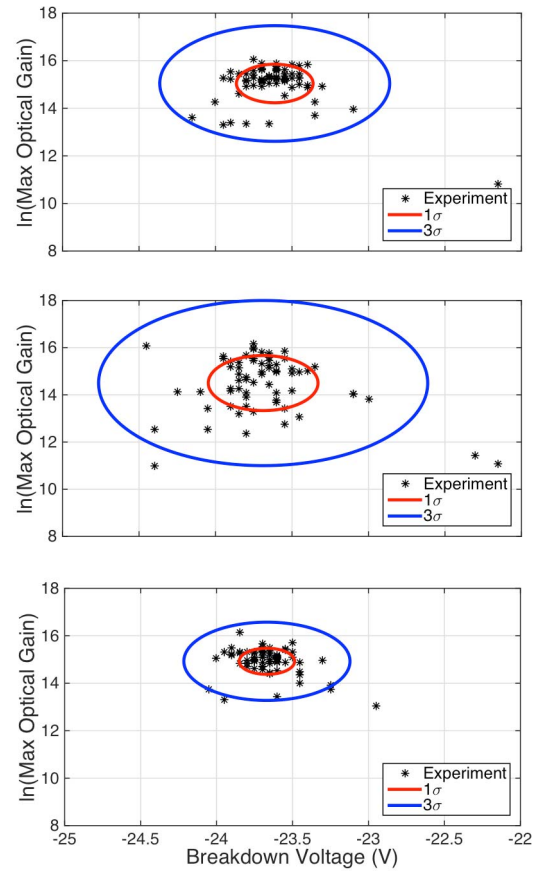


Fig. 10. Spreading of maximum optical gain versus breakdown voltage across 70 devices measured at 940 nm and 77 K. Top: single pixels. Middle: quad pixels. Bottom: hexapixels.

TABLE III  
SUMMARY OF APD PERFORMANCE PARAMETERS FOR  
THE THREE PIXEL TYPES

	Mean value of $V_{bd}$	$\sigma(V_{bd})$	Mean of max. optical gain
Single	-23.69 V	0.36 V	$3.18 \times 10^6$
Quad	-23.61 V	0.25 V	$4.16 \times 10^6$
Hexa	-23.67 V	0.18 V	$3.42 \times 10^6$

inner ellipse for  $\sigma$  and the outer ellipse for  $3\sigma$  spread. The mean values of maximum optical gain and breakdown, as well as the breakdown standard deviation, are listed in Table III. The spreading of  $V_{bd}$  was 1.52%, 1.07%, and 0.78% for single pixels, quadpixels, and hexapixels, respectively. Just as the lower spread around high current levels of the reverse  $I$ - $V$  characteristics of the hexa devices can be explained by the higher probability of perimeter imperfections, this also explains the lower spread with perimeter of the maximum gain to breakdown voltage relationship.

## V. CONCLUSION

The PureGaB Ge-on-Si photodiodes integrated in  $300 \times 1$  pixel arrays were characterized at cryogenic temperatures for operation as proportional APDs. Three different pixel geometries were investigated. The main

differentiating factor is a very different area and perimeter, while the electrical/optical performance is comparable with very high optical gain of up to  $10^6$  measured at 77 K, where the low-voltage dark current is lower than the measurement limit of  $2.5 \times 10^{-2} \mu\text{A}/\text{cm}^2$ . When measured at 77 K, the breakdown voltage spread is low ( $23.6 \pm 0.4$  V), and the quantum efficiency reaches up to 21.9% at the wavelength of 850 nm. The values of responsivity in all three designs decreased by 5% when going from the 850 to 940 nm wavelength due to the decrease in absorption coefficient combined with the limited Ge thickness of  $\sim 0.6 \mu\text{m}$ .

The hexa design, composed of the smallest individual Ge-on-Si diodes, had the smallest total Ge area but the largest perimeter with an average increase in the Ge thickness of  $\sim 30\%$  due mainly to V-groove formation. This effect increases the dark current levels but appears to provide the most desirable performance at cryogenic temperatures in terms of spread in the dark current and the optical gain.

The limited Ge thickness enables a very flat topography that is beneficial for integration in the CMOS. All processing steps are performed with high reliability on equipment already in use in the CMOS production but TMGa is not normally installed on Si/SiGe epitaxy equipment. However, only a small modification of the system is needed for adding Ga deposition to the equipment capabilities. All in all, the PureGaB Ge-on-Si offers a very low-complexity CMOS-compatible means of fabricating uniform arrays of the NIR sensitive photodetectors that are operational in linear, avalanche, and even Geiger modes.

#### ACKNOWLEDGMENT

The authors would like to thank W. Grabinski and Prof. A. Ionescu from the Nanoelectronic Devices Group at École Polytechnique Fédérale de Lausanne, Lausanne, Switzerland, and ID Quantique SA for electrical and optical measurement support, and the staff of the DIMES cleanrooms for processing support.

#### REFERENCES

- [1] M. Aminian, A. Sammak, L. Qi, L. Nanver, and E. Charbon, "A Ge-on-Si single-photon avalanche diode operating in Geiger mode at infrared wavelengths," *Proc. SPIE*, vol. 8375, p. 83750Q, May 2012.
- [2] V. Saveliev, "The recent development and study of silicon photomultiplier," *Nucl. Instrum. Methods Phys. Res. A, Accel. Spectrom., Detectors Assoc. Equip.*, vol. 535, nos. 1–2, pp. 528–532, Dec. 2004.
- [3] E. Charbon, "Single-photon imaging in complementary metal-oxide-semiconductor processes," *Philos. Trans. Roy. Soc. London A, Math. Phys. Eng. Sci.*, vol. 372, no. 2012, p. 20130100, 2014.
- [4] A. Tosi, A. D. Mora, S. Tisa, F. Acerbi, F. Zappa, and S. Cova, "InGaAs/InP SPADs for near-infrared applications: Device operating conditions and dedicated electronics," *Proc. SPIE*, vol. 7681, p. 76810R, Apr. 2010.
- [5] L. Vivien *et al.*, "42 GHz p.i.n Germanium photodetector integrated in a silicon-on-insulator waveguide," *Opt. Exp.*, vol. 17, no. 8, pp. 6252–6257, 2009.
- [6] G. Masini, V. Cencelli, L. Colace, F. De Notaristefani, and G. Assanto, "Monolithic integration of near-infrared Ge photodetectors with Si complementary metal-oxide-semiconductor readout electronics," *Appl. Phys. Lett.*, vol. 80, no. 18, p. 3268, 2002.
- [7] A. Sammak, L. Qi, and L. K. Nanver, "Restricted-access Al-mediated material transport in Al contacting of PureGaB Ge-on-Si p+n diodes," *J. Electron. Mater.*, to be published.
- [8] A. Sammak, M. Aminian, L. Qi, W. B. De Boer, E. Charbon, and L. K. Nanver, "Fabrication of PureGaB Ge-on-Si photodiodes for well-controlled 100-pA-level dark currents," *Electro-Chem. Soc. Trans.*, vol. 64, no. 6, pp. 737–745, 2014.

- [9] A. Sammak, M. Aminian, L. Qi, E. Charbon, and L. K. Nanver, "A  $270 \times 1$  Ge-on-Si photodetector array for sensitive infrared imaging," *Proc. SPIE*, vol. 9141, p. 914104, May 2014.
- [10] A. Sammak, M. Aminian, L. Qi, W. B. De Boer, E. Charbon, and L. K. Nanver, "A CMOS compatible Ge-on-Si APD operating in proportional and Geiger modes at infrared wavelengths," in *Proc. IEDM*, Washington, DC, USA, Dec. 2011, pp. 8.5.1–8.5.4.
- [11] I. P. Santos, P. J. Caspers, T. B. Schut, R. V. Doorn, S. Koljenović, and G. J. Puppels, "Implementation of a novel low-noise InGaAs detector enabling rapid near-infrared multichannel Raman spectroscopy of pigmented biological samples," *J. Raman Spectrosc.*, to appear, 2015.
- [12] A. Sammak, W. B. De Boer, and L. K. Nanver, "Ge-on-Si: Single-crystal selective epitaxial growth in a CVD reactor," *Electro-Chem. Soc. Trans.*, vol. 50, no. 9, pp. 507–512, 2012.
- [13] Y. Civalé, L. K. Nanver, P. Hadley, E. J. G. Goudena, and H. Schellevis, "Sub-500 °C solid-phase epitaxy of ultra-abrupt p<sup>+</sup>-silicon elevated contacts and diodes," *IEEE Electron Device Lett.*, vol. 27, no. 5, pp. 341–343, May 2006.
- [14] C. Belove, Ed., *Handbook of Modern Electronics and Electrical Engineering*. New York, NY, USA: Wiley, 1986.
- [15] R. E. Warburton *et al.*, "Ge-on-Si single-photon avalanche diode detectors: Design, modeling, fabrication, and characterization at wavelengths 1310 and 1550 nm," *IEEE Trans. Electron Devices*, vol. 60, no. 11, pp. 3807–3813, Nov. 2013.
- [16] L. K. Nanver *et al.*, "Robust UV/VUV/EUV pureB photodiode detector technology with high CMOS compatibility," *IEEE J. Sel. Topics Quantum Electron.*, vol. 20, no. 6, Nov./Dec. 2014, Art. ID 3801711.
- [17] Y. Kang *et al.*, "Monolithic germanium/silicon avalanche photodiodes with 340 GHz gain-bandwidth product," *Nature Photon.*, vol. 3, no. 1, pp. 59–63, 2009.
- [18] T. H. Loh *et al.*, "Selective epitaxial germanium on silicon-on-insulator high speed photodetectors using low-temperature ultrathin Si<sub>0.8</sub>Ge<sub>0.2</sub> buffer," *Appl. Phys. Lett.*, vol. 91, no. 7, p. 073503, 2007.
- [19] M. Takenaka, K. Morii, M. Sugiyama, Y. Nakano, and S. Takagi, "Dark current reduction of Ge photodetector by GeO<sub>2</sub> surface passivation and gas-phase doping," *Opt. Exp.*, vol. 20, no. 8, pp. 8718–8725, 2012.
- [20] I. Åberg *et al.*, "A low dark current and high quantum efficiency monolithic germanium-on-silicon CMOS imager technology for day and night imaging applications," in *Proc. IEDM*, San Francisco, CA, USA, Dec. 2010, pp. 14.4.1–14.4.4.



**Amir Sammak** was born in Tehran, Iran, in 1983. He received the B.Sc. and M.Sc. degrees in electrical engineering from the University of Tehran, Tehran, in 2005 and 2007, respectively, and the Ph.D. degree from the Faculty of Electrical Engineering, Mathematics and Computer Science, Delft University of Technology, Delft, The Netherlands, in 2012.

He is currently a Post-Doctoral Research Fellow with the Delft University of Technology.



**Mahdi Aminian** was born in Tehran, Iran. He received the bachelor's degree in computer engineering and the master's degree in computer architecture from the Amirkabir University, Tehran, in 2006 and 2008, respectively, and the Ph.D. degree from the Department of Electrical Engineering, École Polytechnique Fédérale de Lausanne, Lausanne, Switzerland, in 2014.

He is currently involved in Ge-on-Si single photon image sensors and Si deep junction sensors for near-infrared detection.





**Lis K. Nanver** (M'80) received the master's degree in physics from the University of Aarhus, Aarhus, Denmark, in 1979, and the Doctor-Engineer degree in physics applied to telecommunications from the Ecole Nationale Supérieure des Télécommunications, Paris, France, in 1982, and the Ph.D. degree in electrical engineering from the Delft University of Technology, Delft, The Netherlands, in 1987.

She is currently with Aalborg University, Aalborg, Denmark and also the University of Twente, Enschede, The Netherlands.



**Edoardo Charbon** (M'92–SM'10) received the Diploma degree from ETH Zurich, Zurich, Switzerland, in 1988, the M.S. degree from the University of California at San Diego, La Jolla, in 1991, and the Ph.D. degree from the University of California at Berkeley, Berkeley, CA, USA, in 1995, all in electrical engineering and electrical engineering and computer sciences.

He has been with the École Polytechnique Fédérale de Lausanne, Lausanne, Switzerland, since 2002 and Delft University of Technology, Delft,

The Netherlands, since 2008.